

09/788,061, 021603

**FIG. 1**

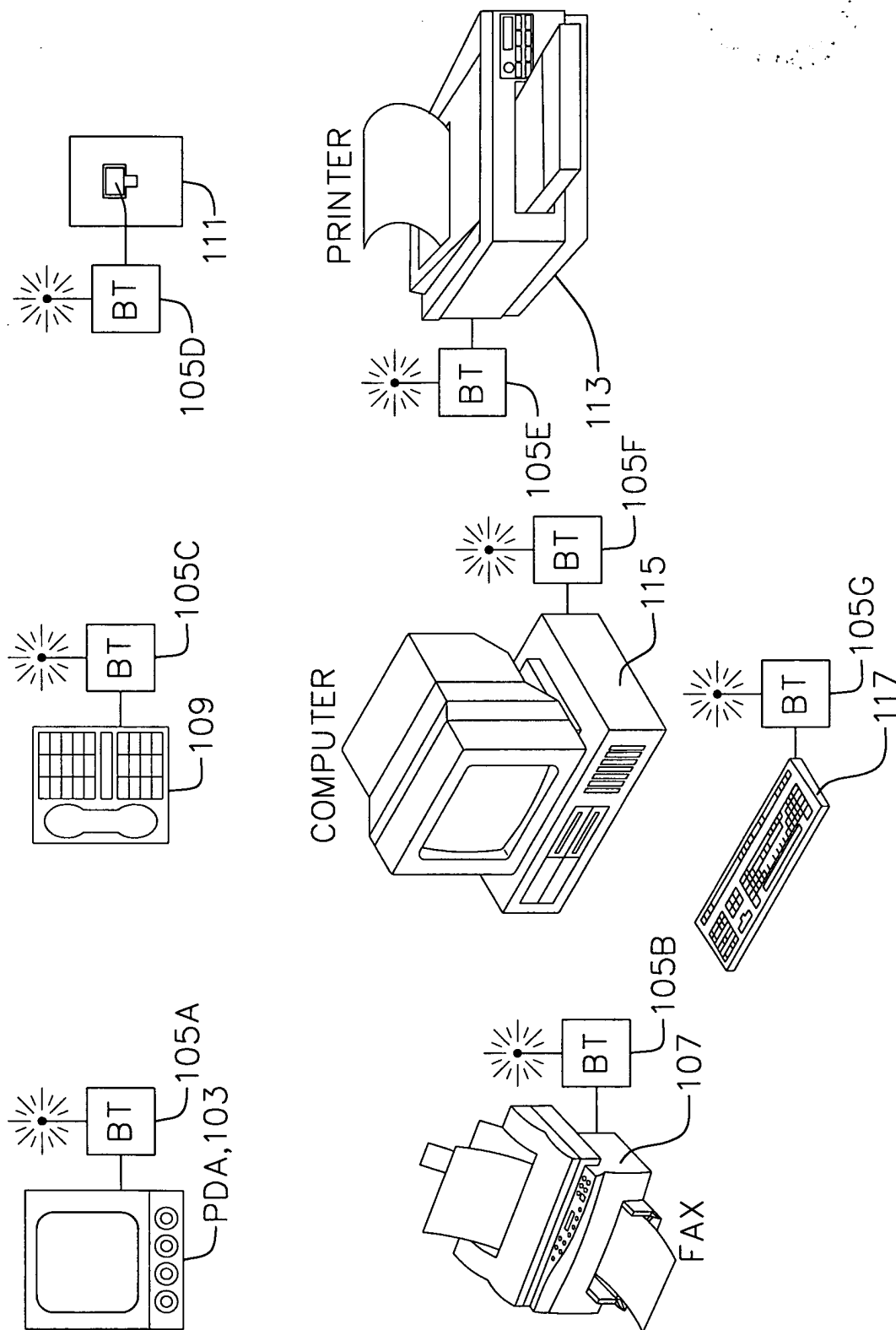


FIG. 2A

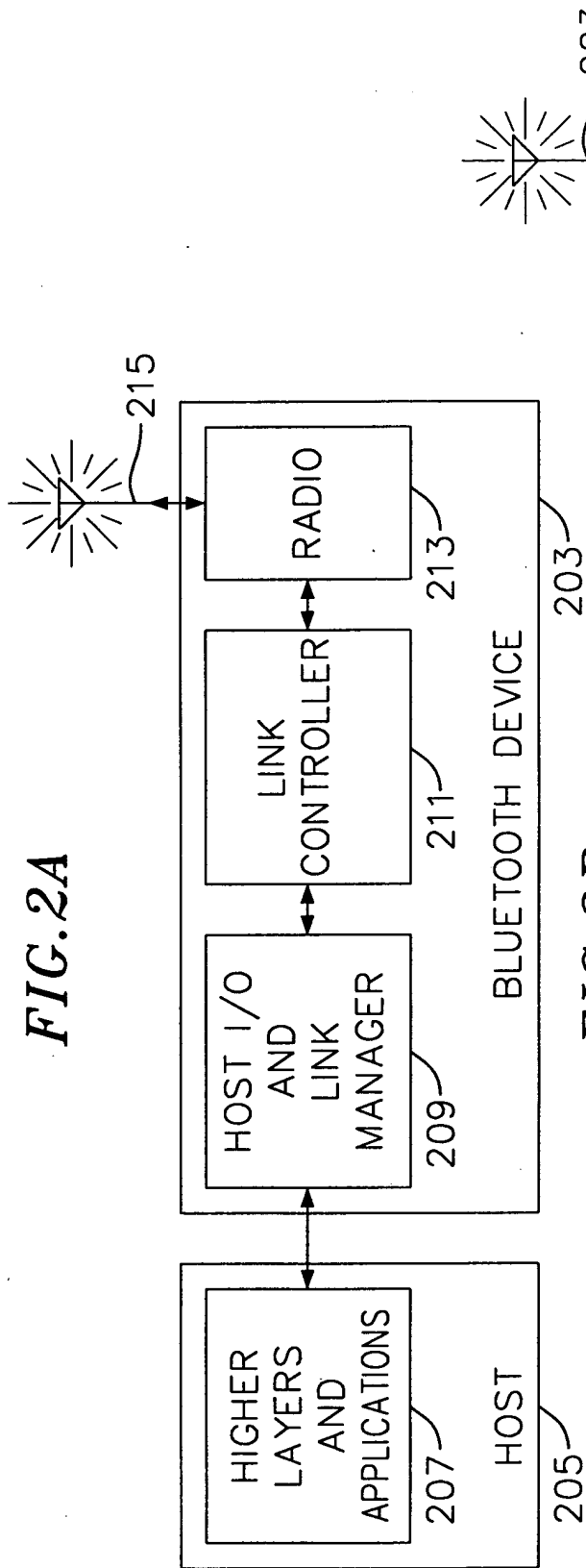
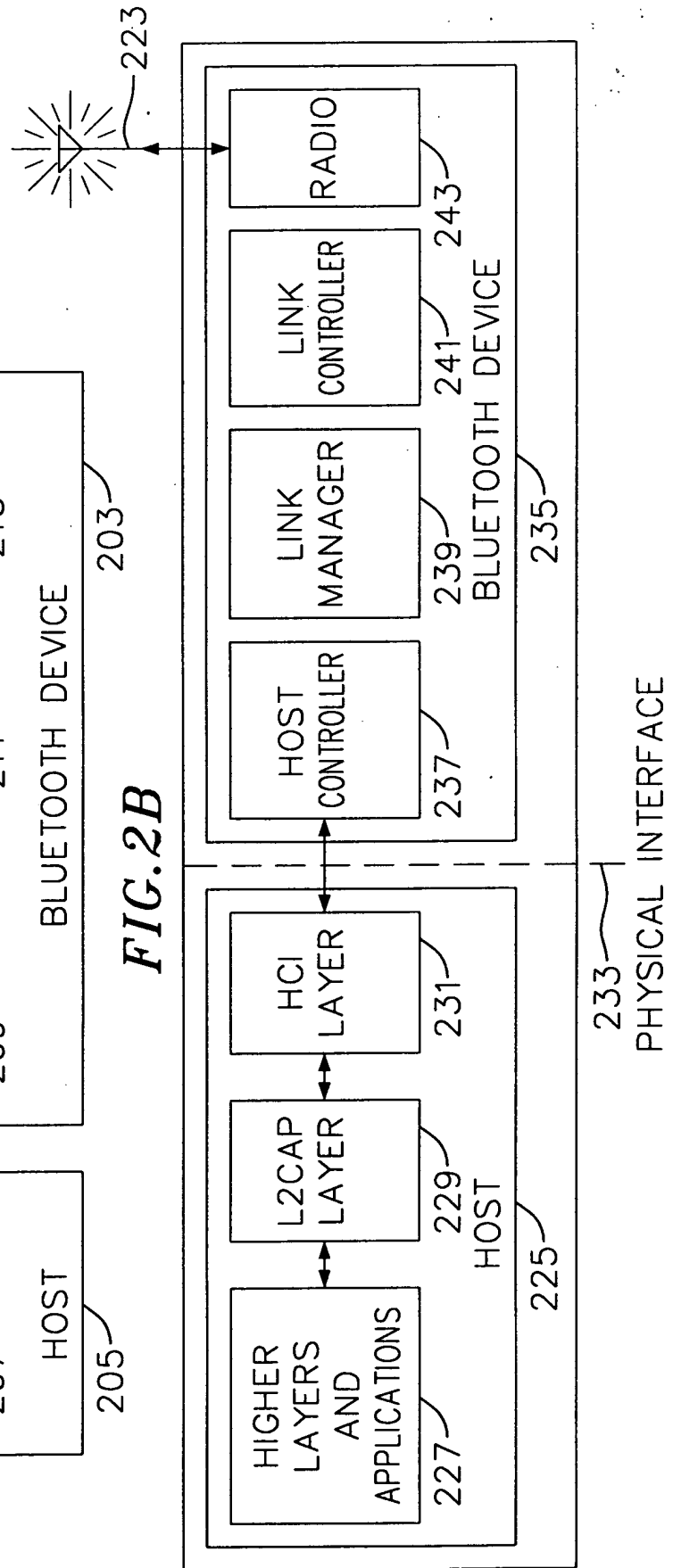
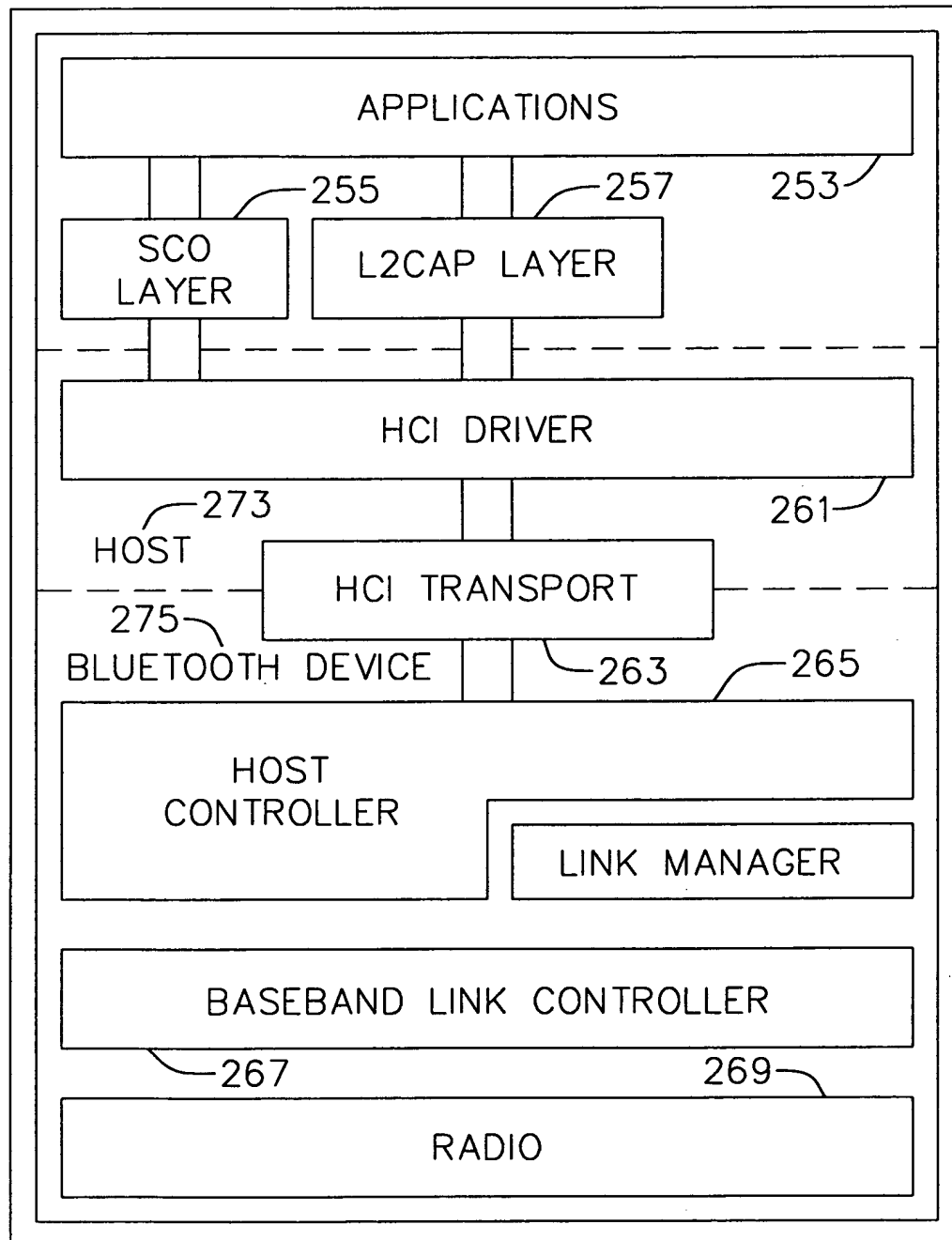


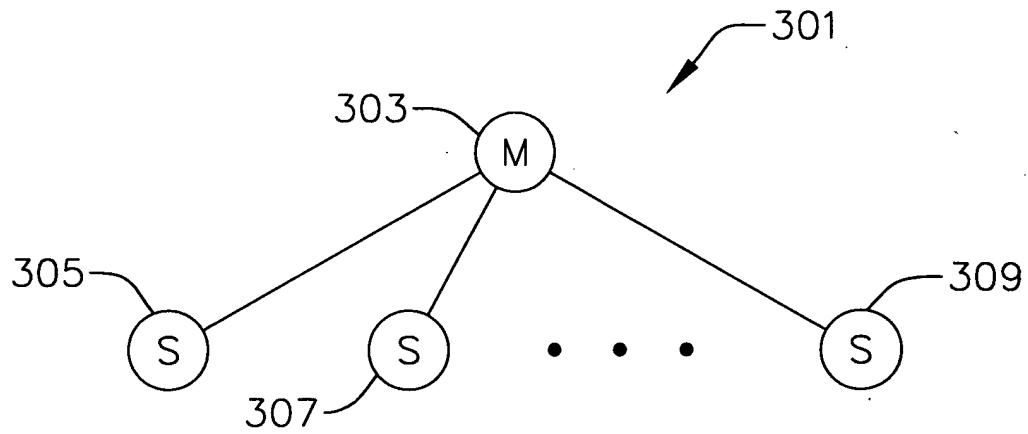
FIG. 2B



*FIG. 2C*



*FIG. 3*



*FIG. 4*

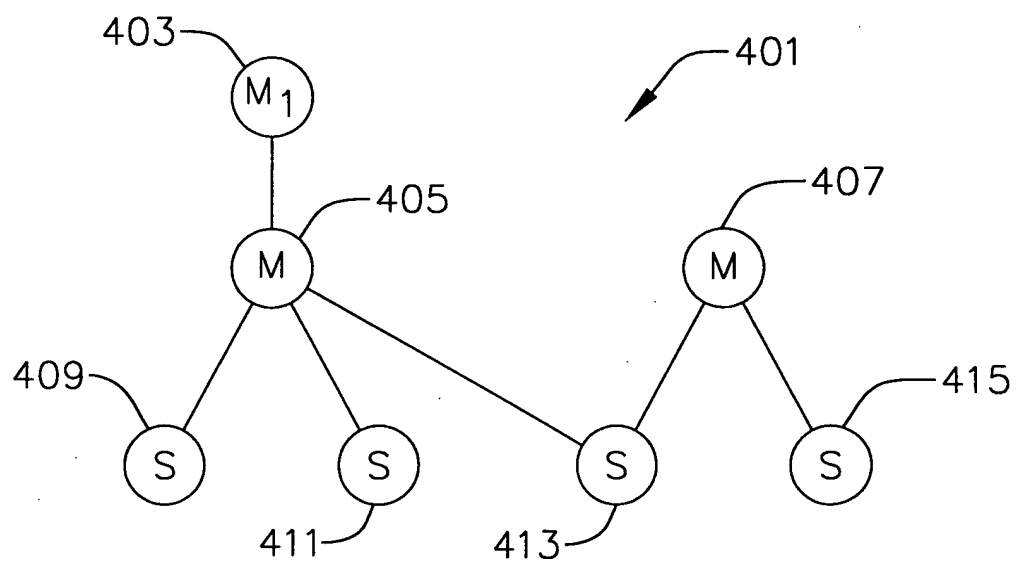
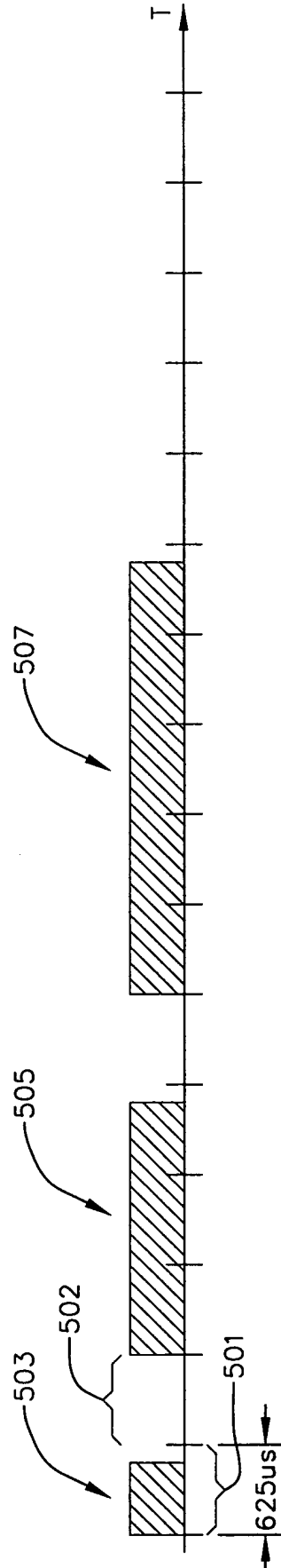
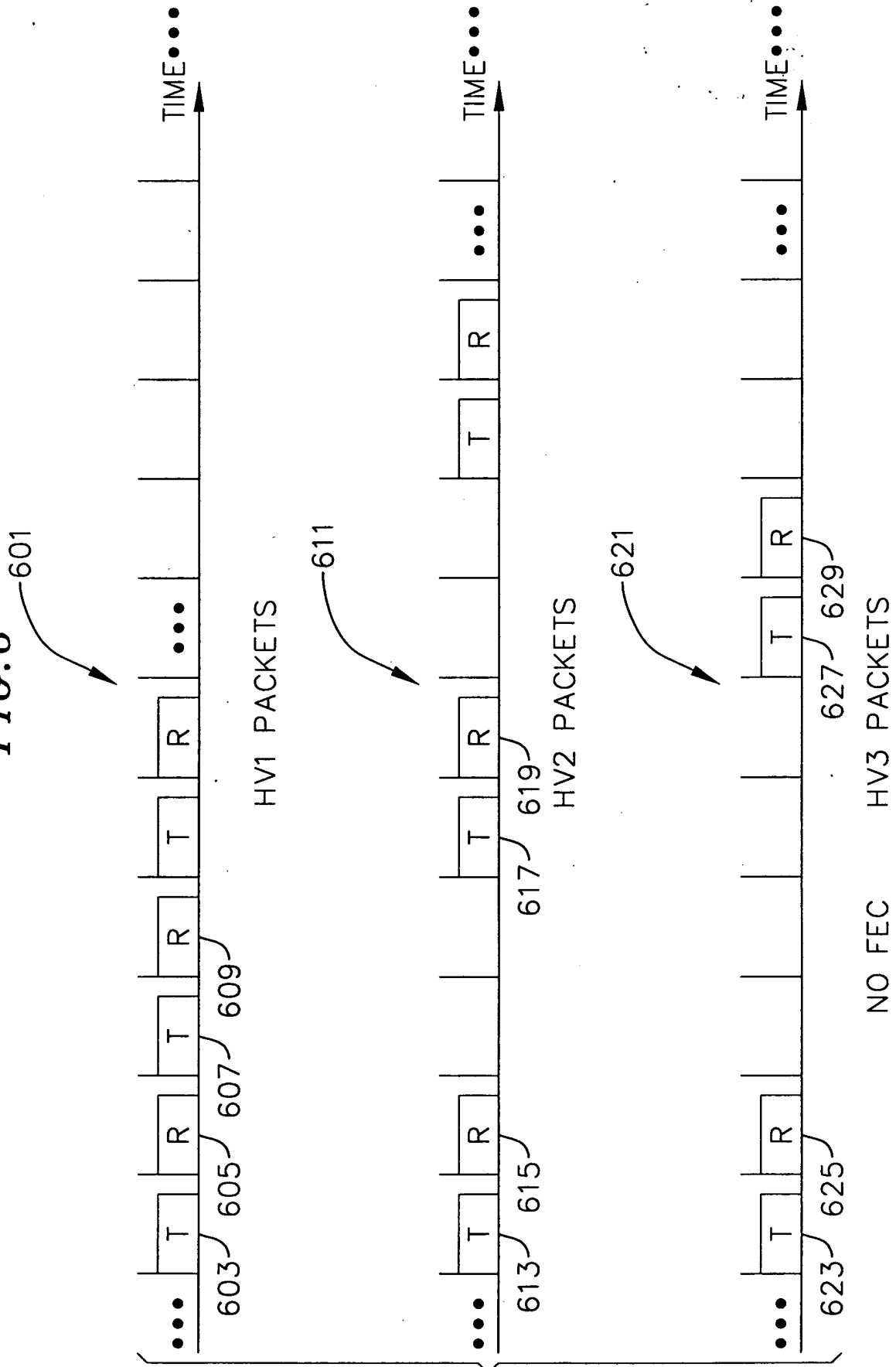


FIG. 5

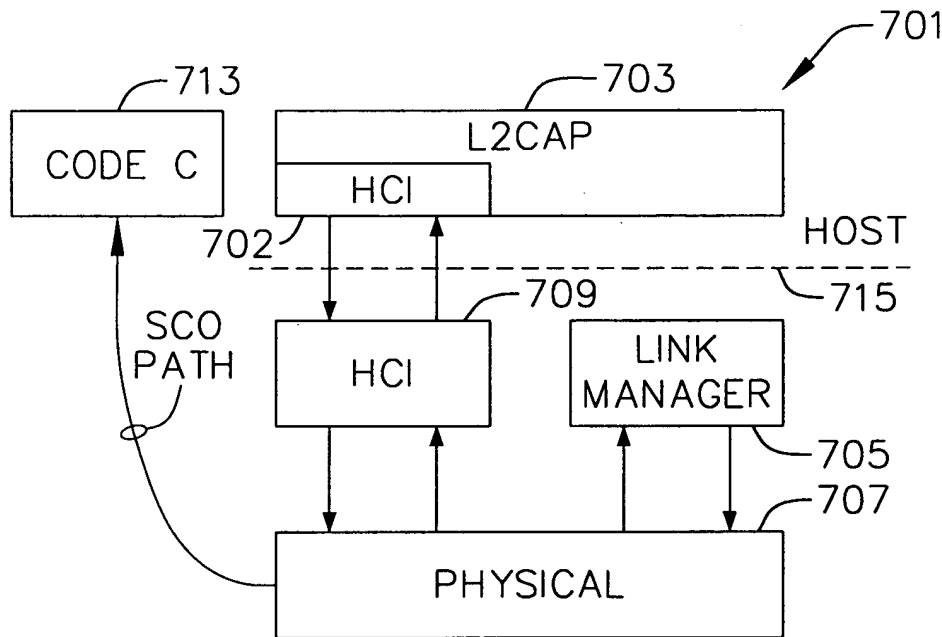


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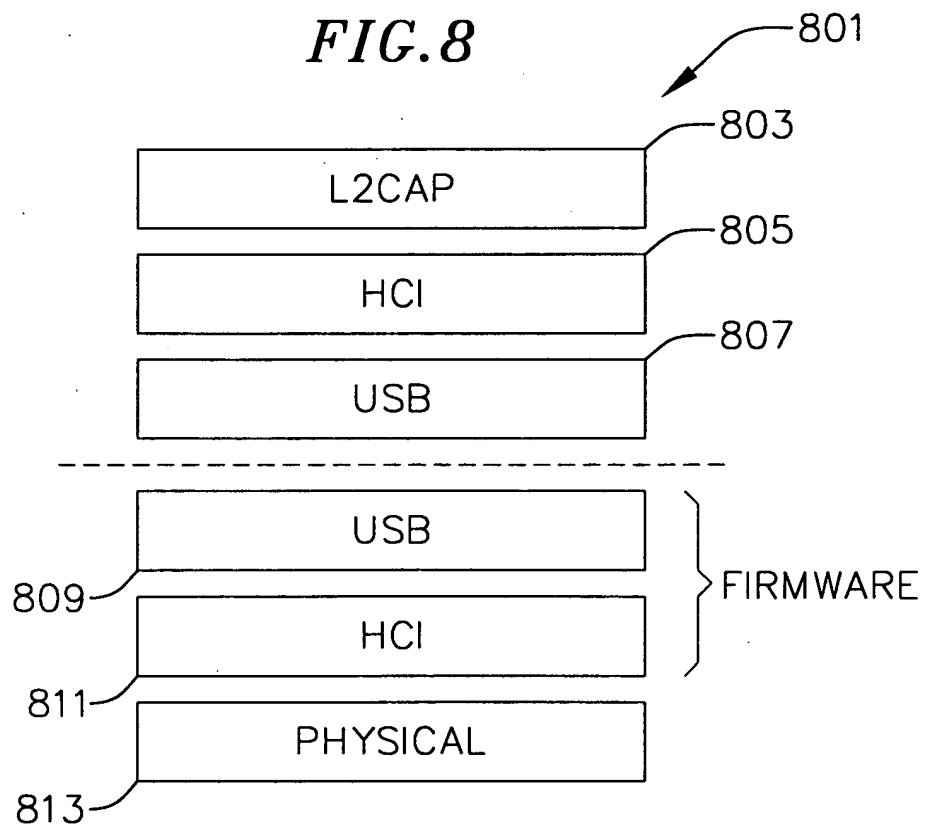
FIG. 6



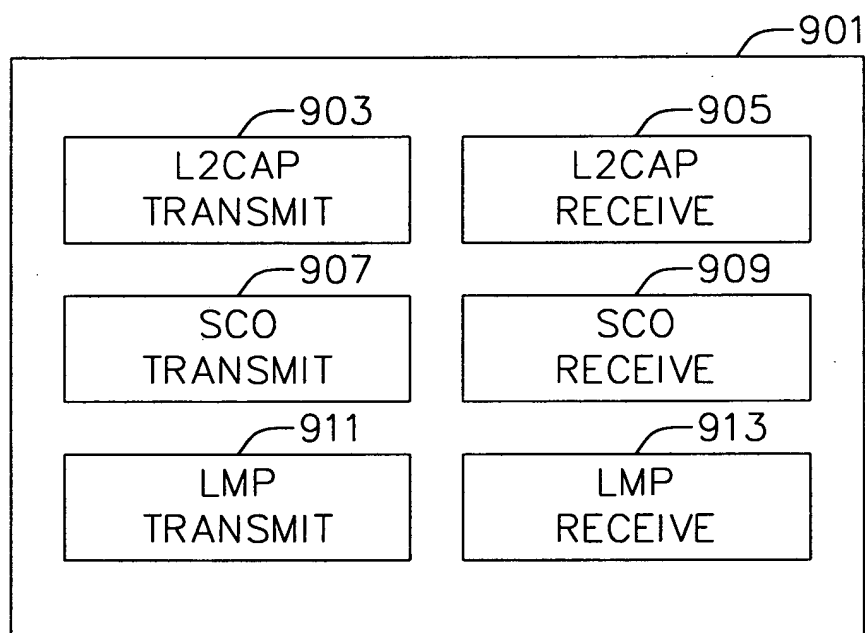
**FIG. 7**



**FIG. 8**

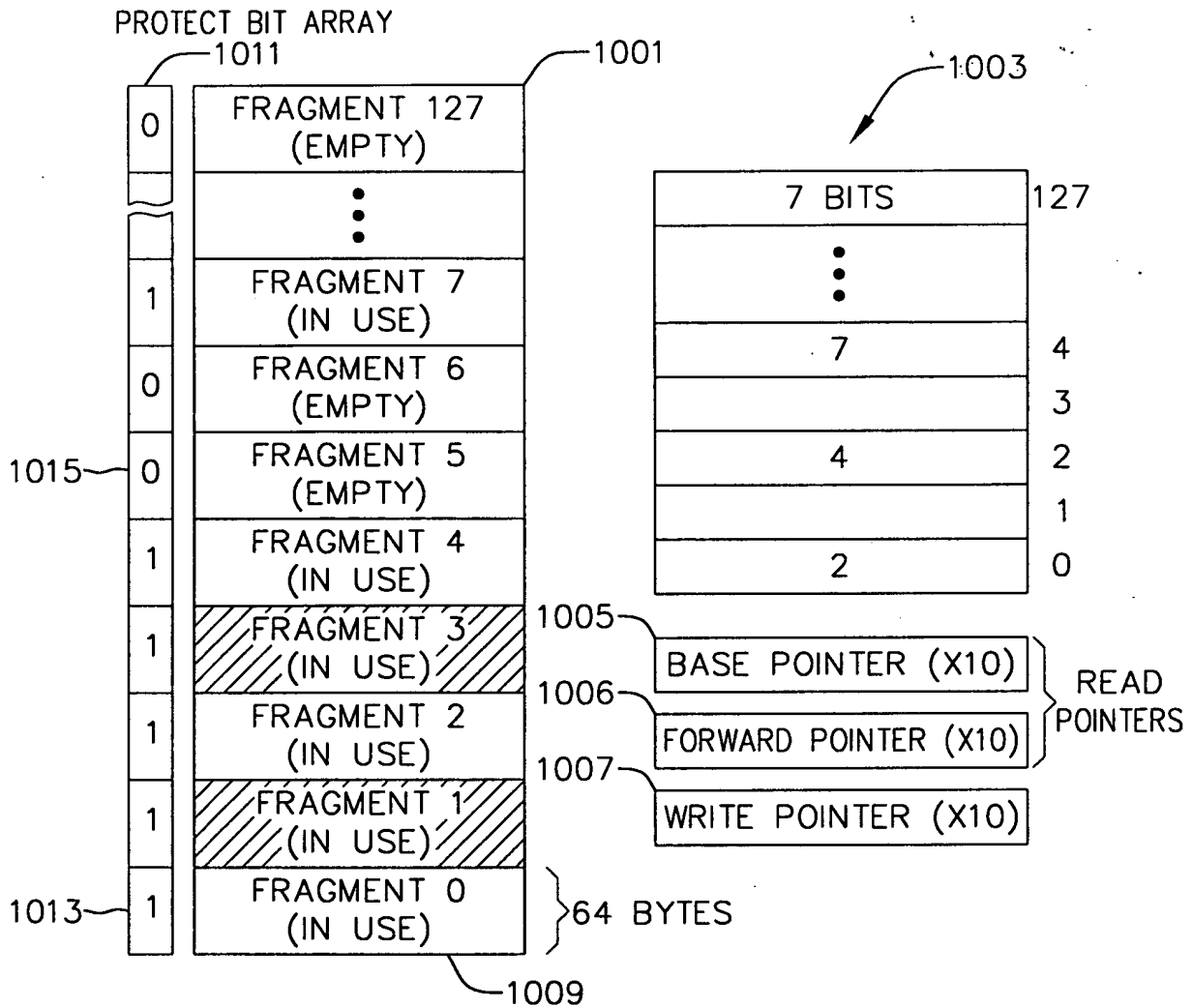


*FIG. 9*

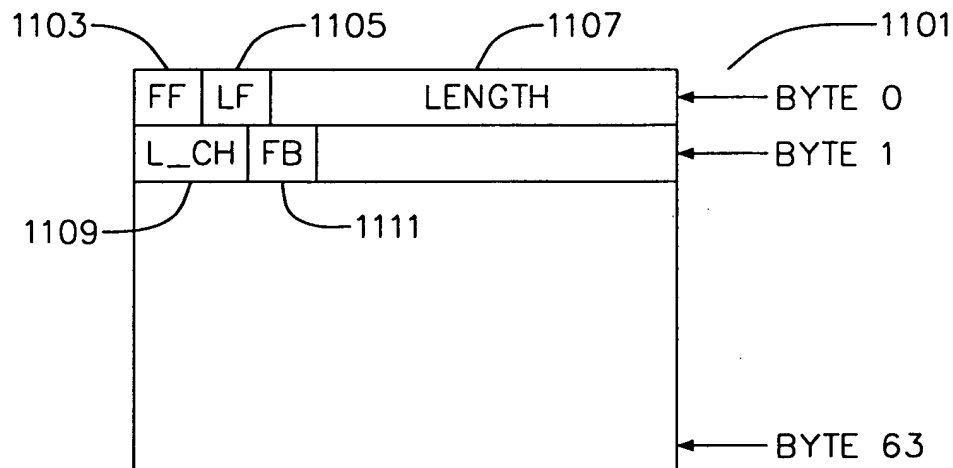




**FIG. 10**



**FIG. 11**



**FIG. 12**

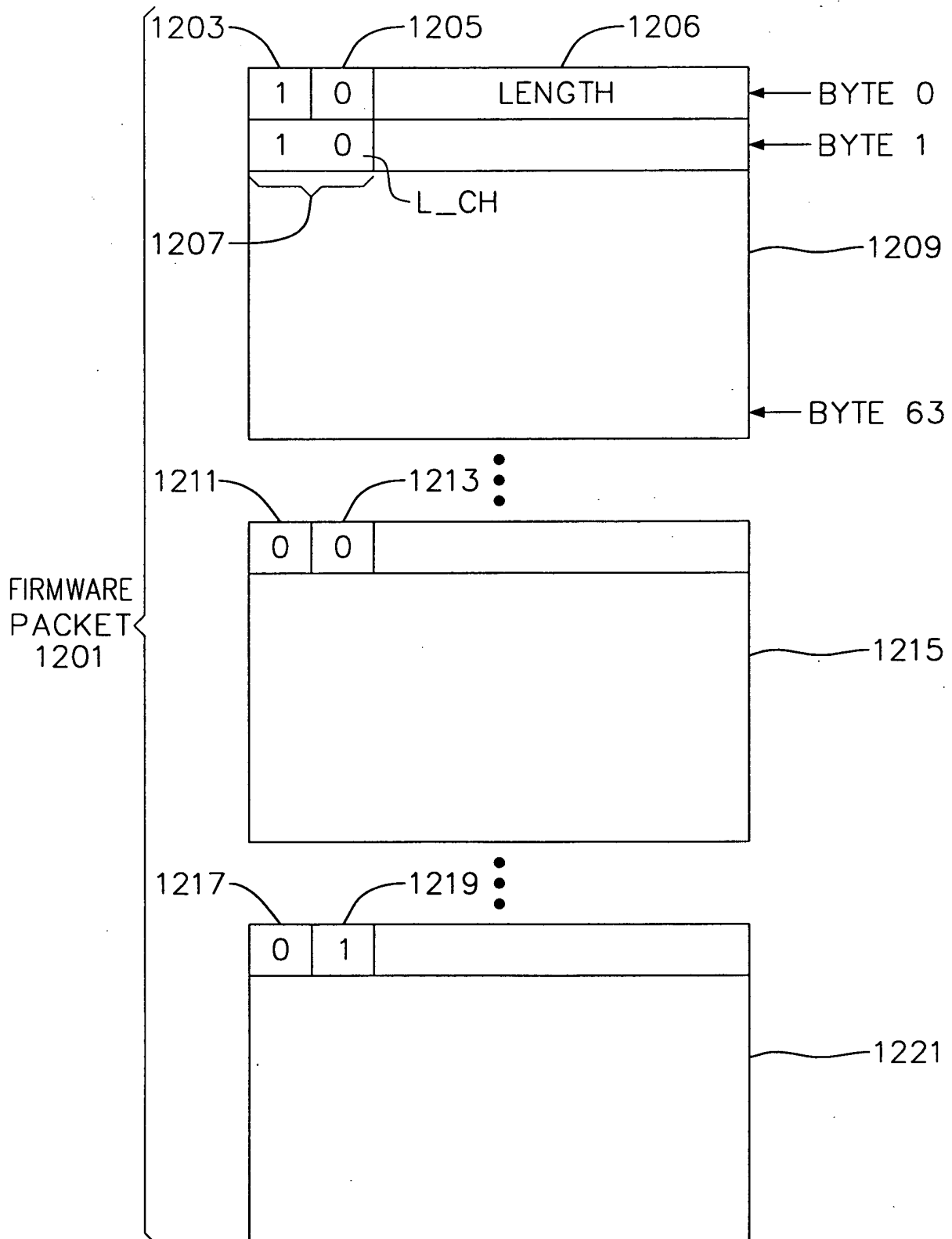


FIG. 13

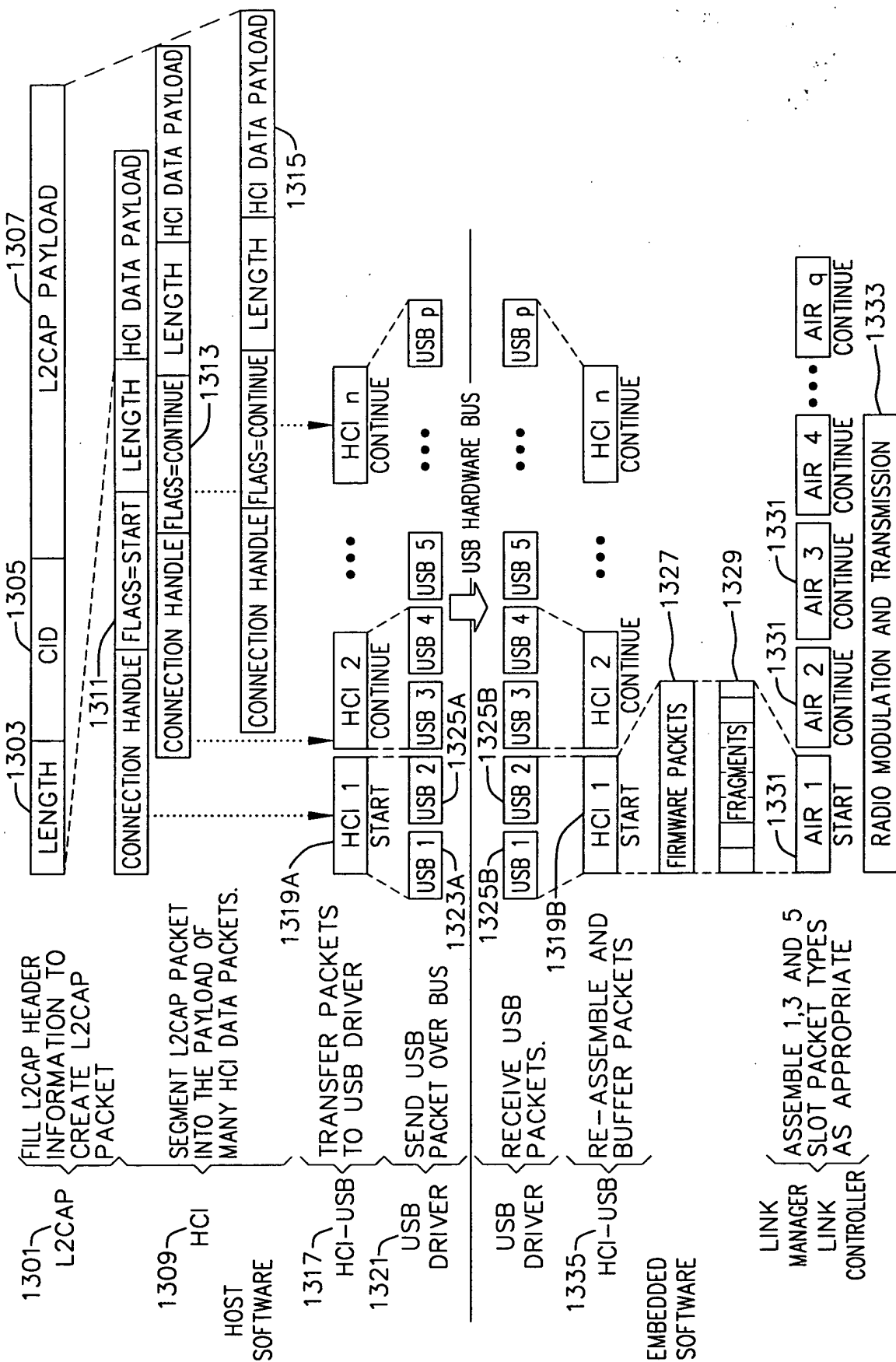
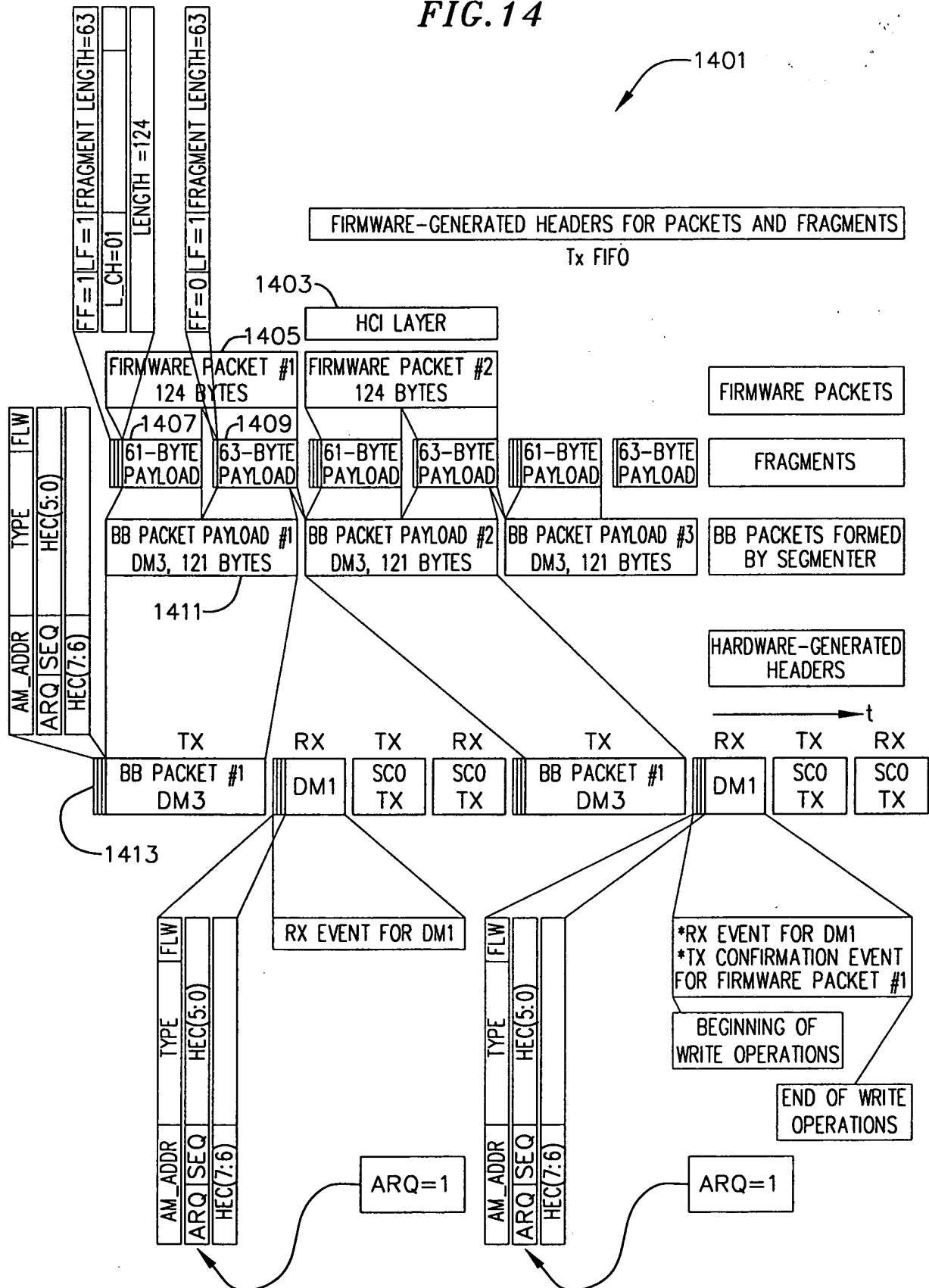


FIG. 14



*FIG. 15*

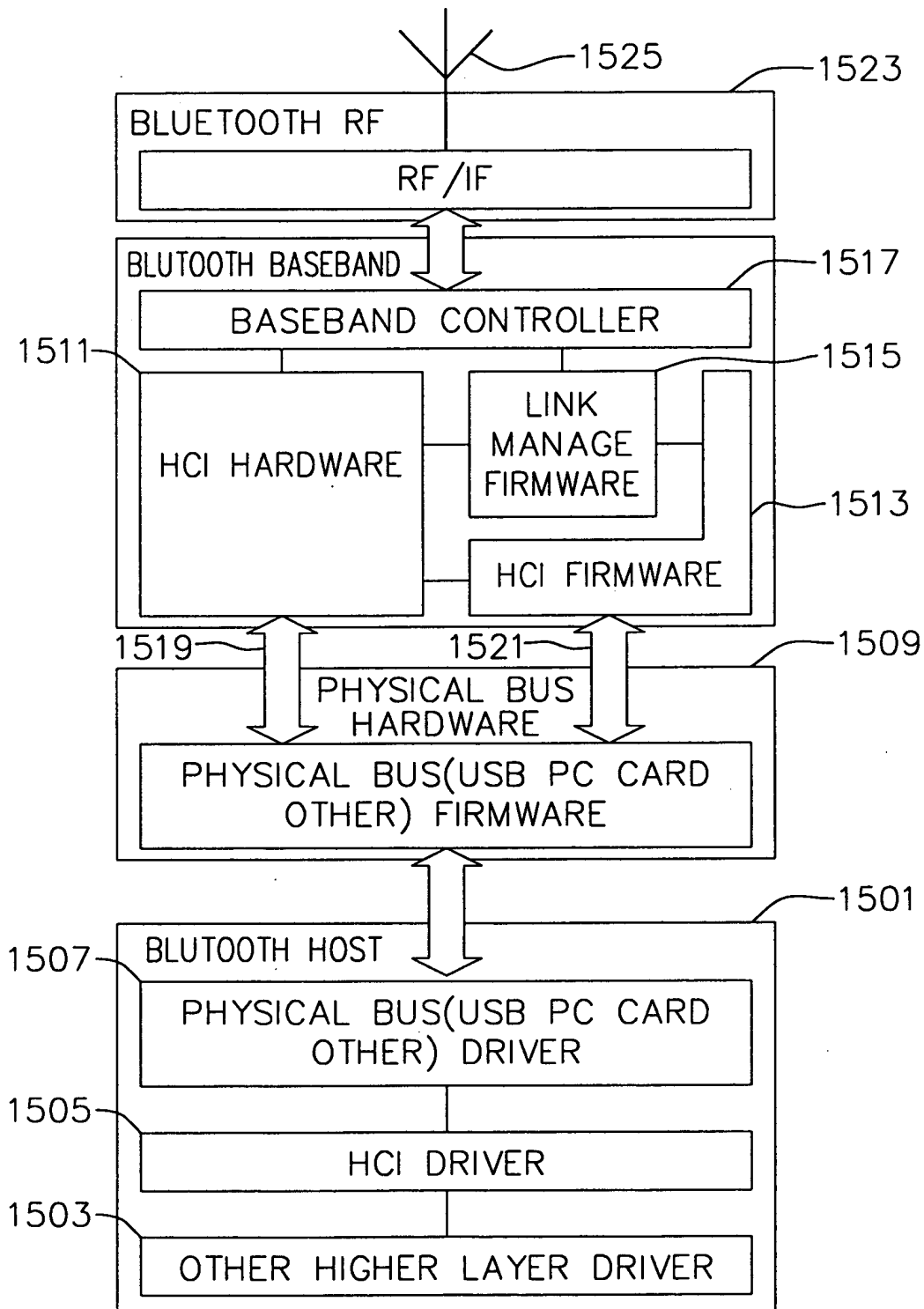
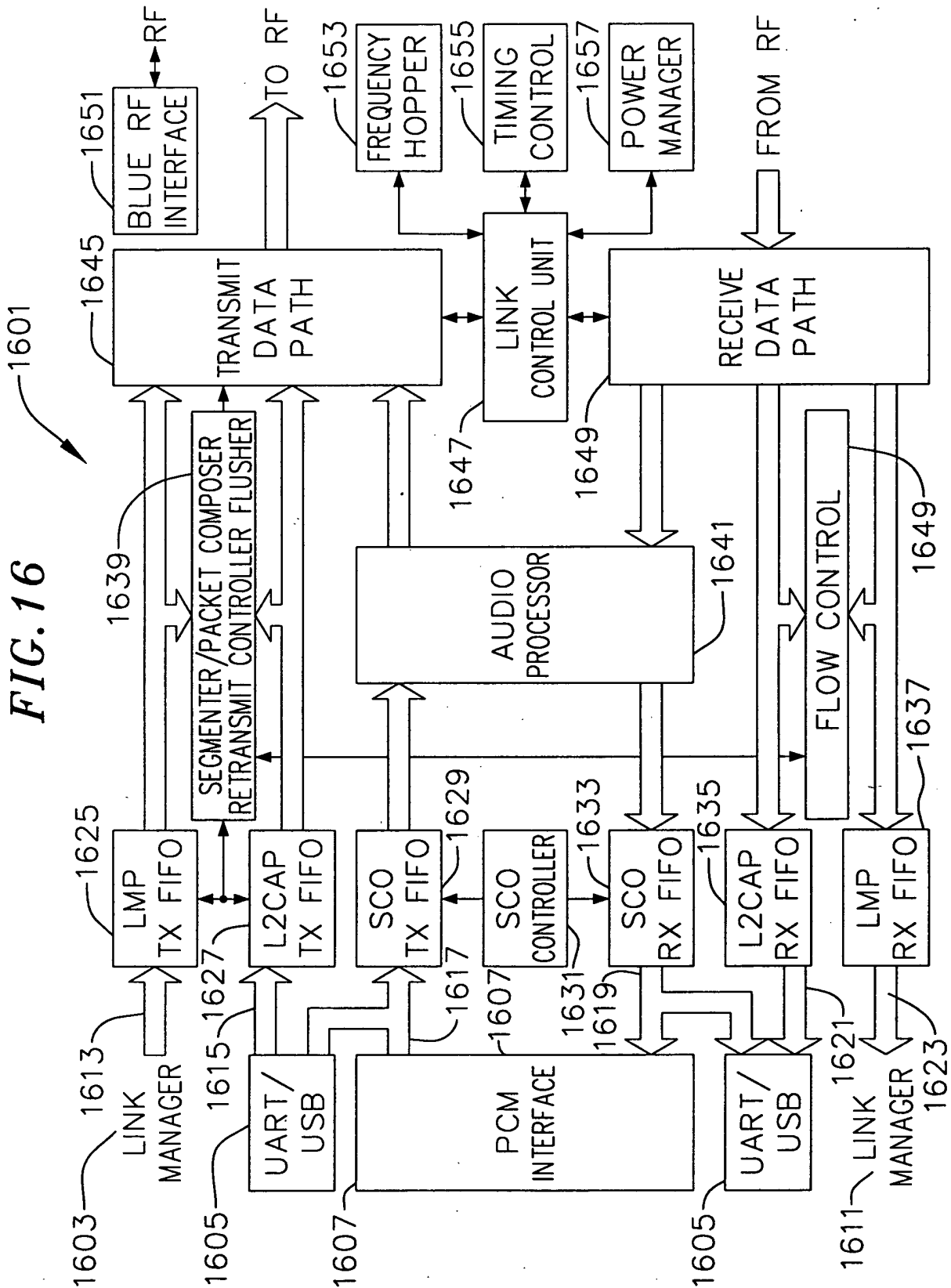


FIG. 16



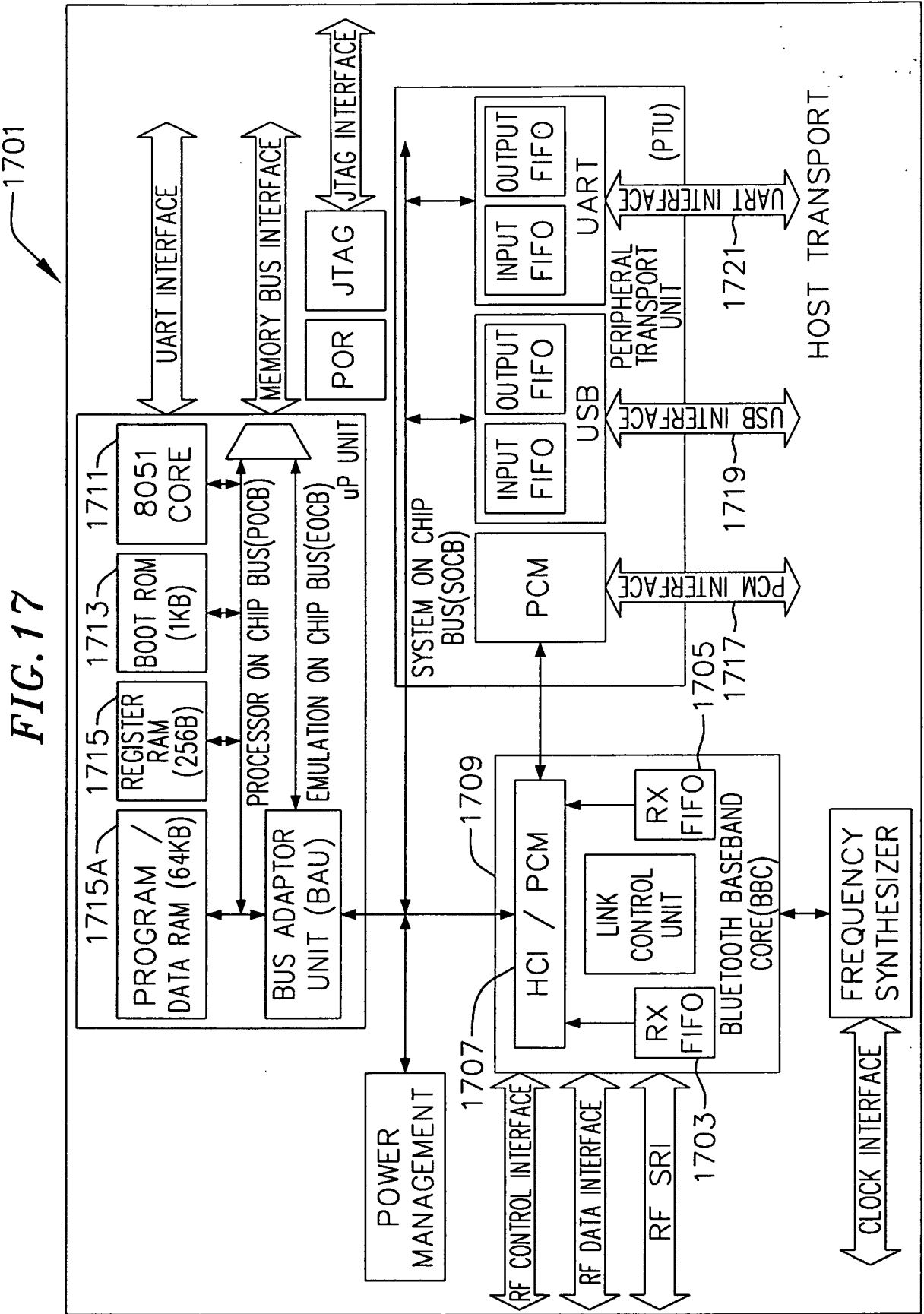
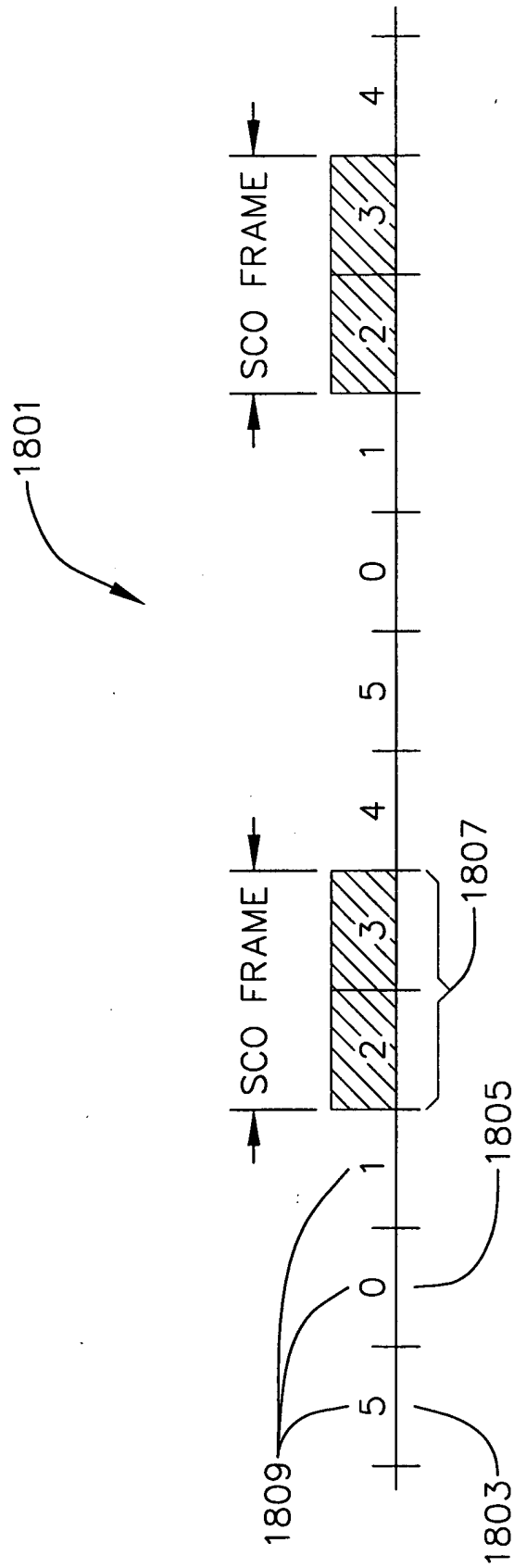
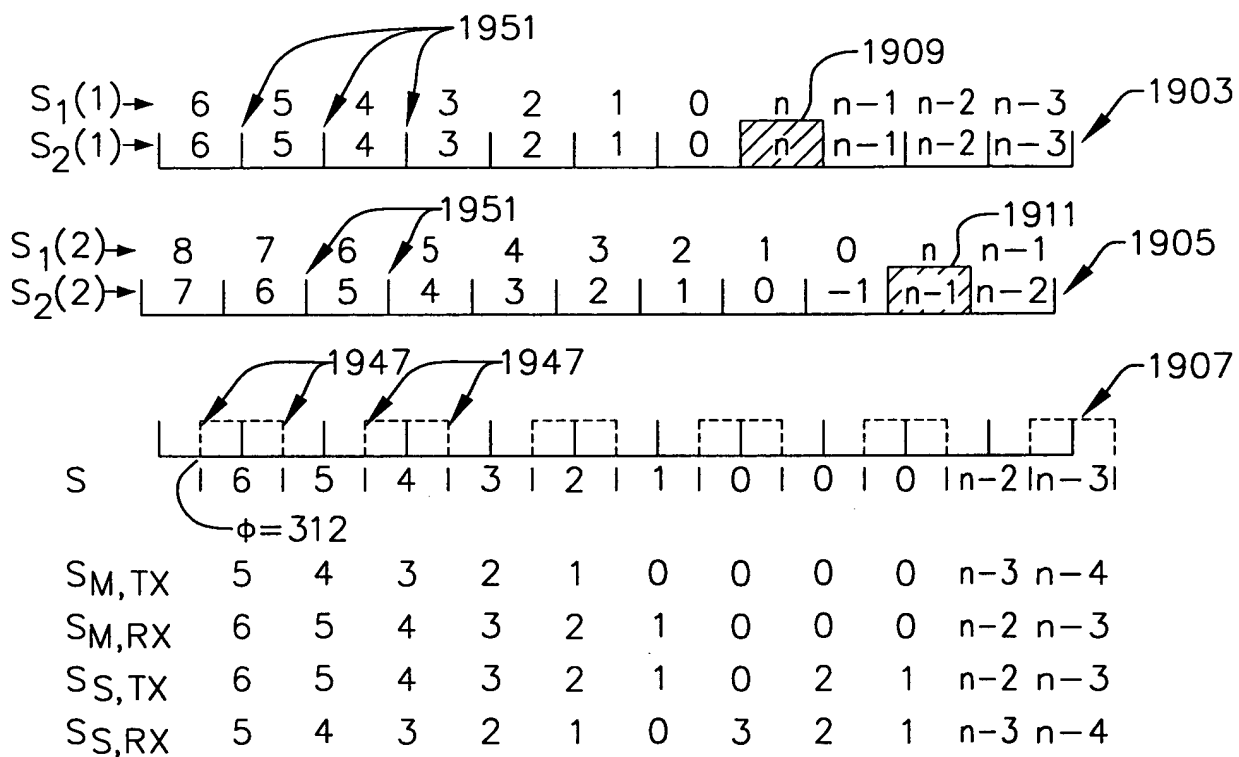


FIG. 18

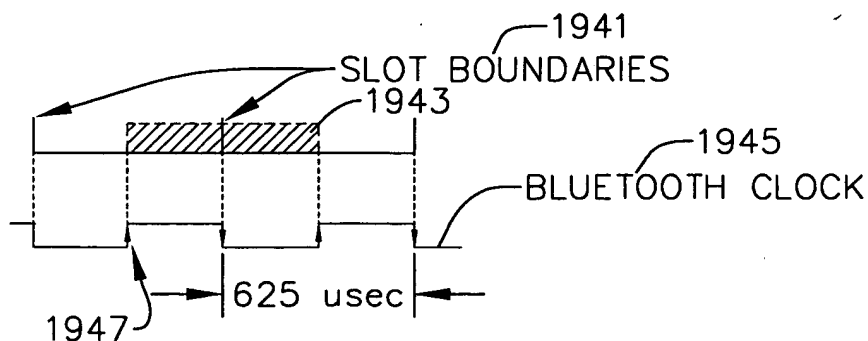




**FIG. 19A**



**FIG. 19B**



**FIG. 20**

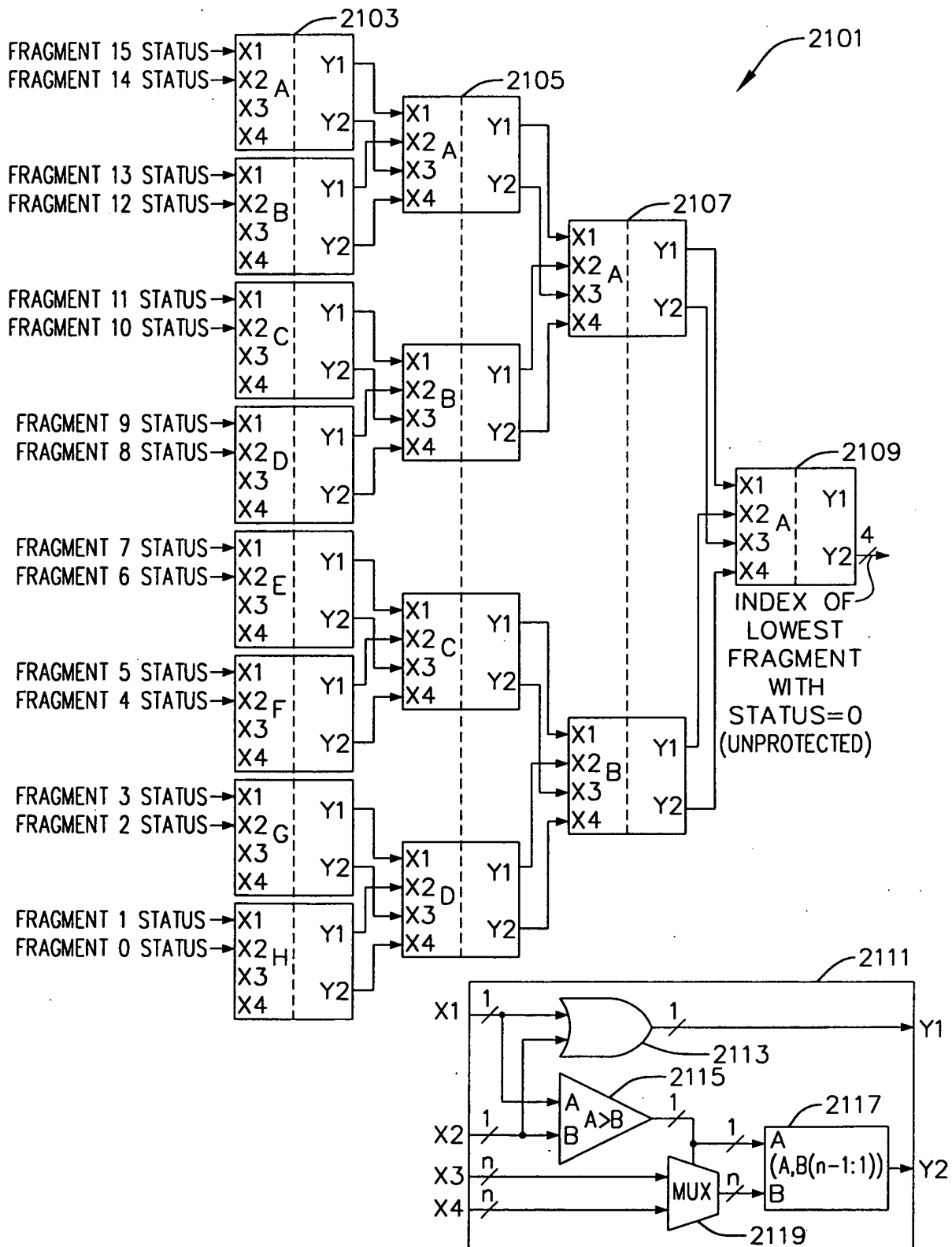
TABLE 1. PACKET TYPE PRIORITY

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RANGE LABEL	MIN BYTES IN BUFFER	MAX BYTES IN BUFFER	1ST. CHOICE	2ND. CHOICE	3RD. CHOICE	4TH. CHOICE	5TH. CHOICE	6TH. CHOICE
a	0	0	NULL	NULL	NULL	NULL	NULL	NULL
b	1	17	DM1	DH1	DM3	DH3	DM5	DH5
c	18	27	DH1	DM3	DH3	DM5	DH5	DM1
d	28	121	DM3	DH3	DM5	DH5	DH1	DM1
e	122	183	DH3	DM5	DH5	DM3	DH1	DM1
f	184	224	DM5	DH5	DH3	DM3	DH1	DM1
g	225	339	DH5	DM5	DH3	DM3	DH1	DM1
h	339		DH5	DM5	DH3	DM3	DH1	DM1

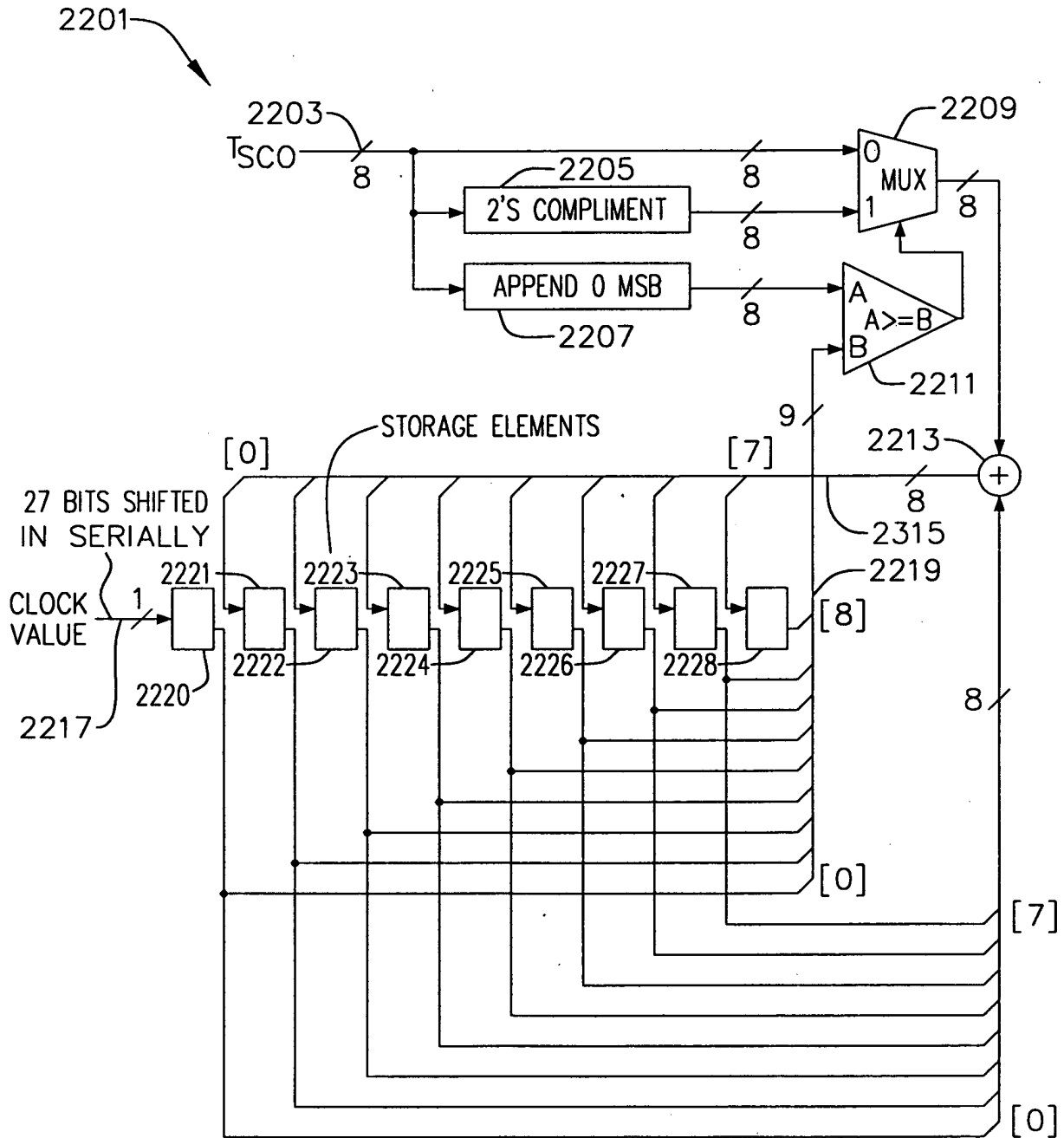
**FIG. 21**

EXAMPLE OF A FRAGMENT CHOOSER FOR 16 FRAGMENTS,  $N=4$   
 (FOR 127 FRAGMENTS  $N=7$ )



**FIG. 22**

CIRCUIT TO CALCULATE CLK MOD Y, WHERE CLK IS 27 BITS AND T IS 8 BITS



**FIG. 23**

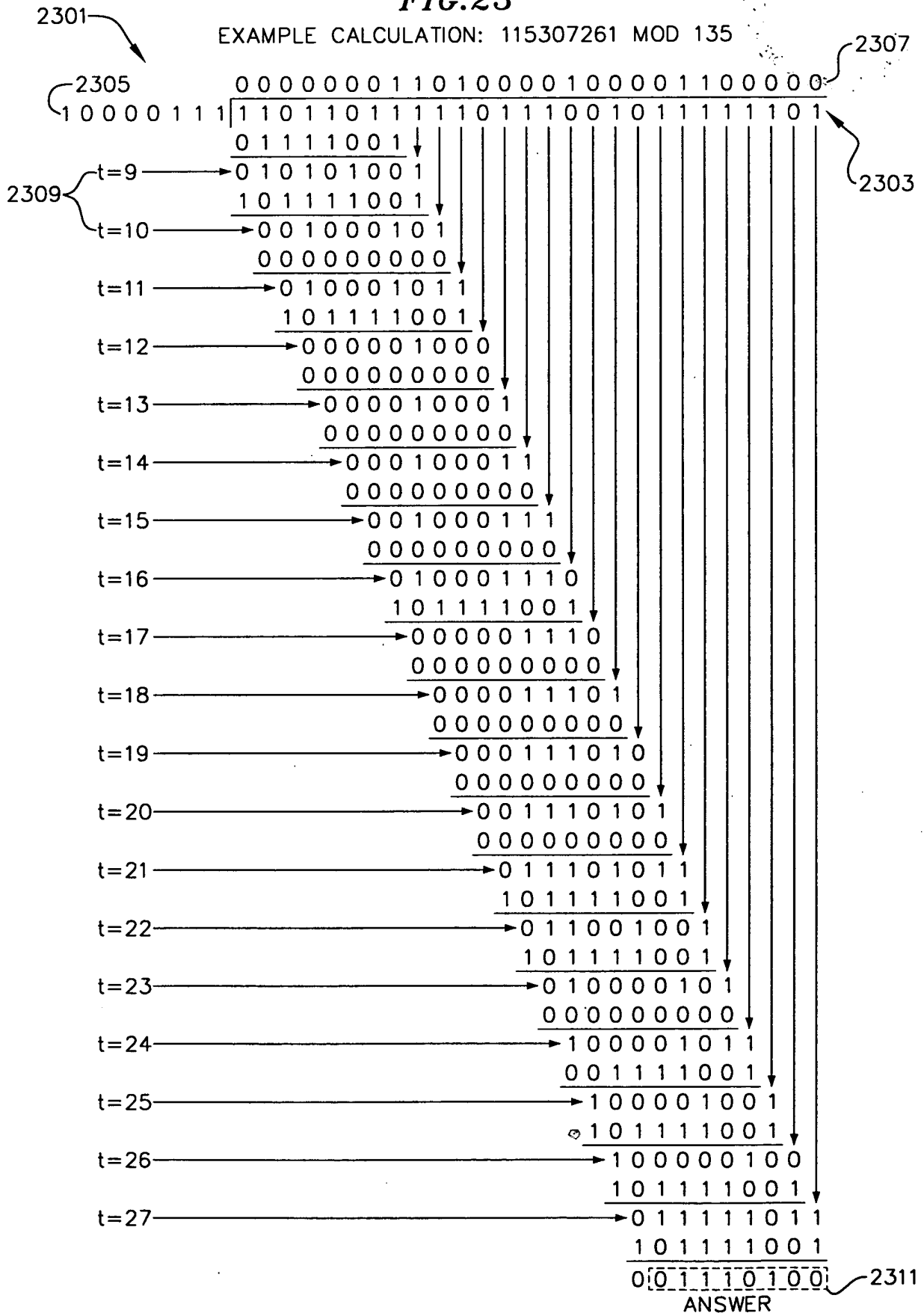
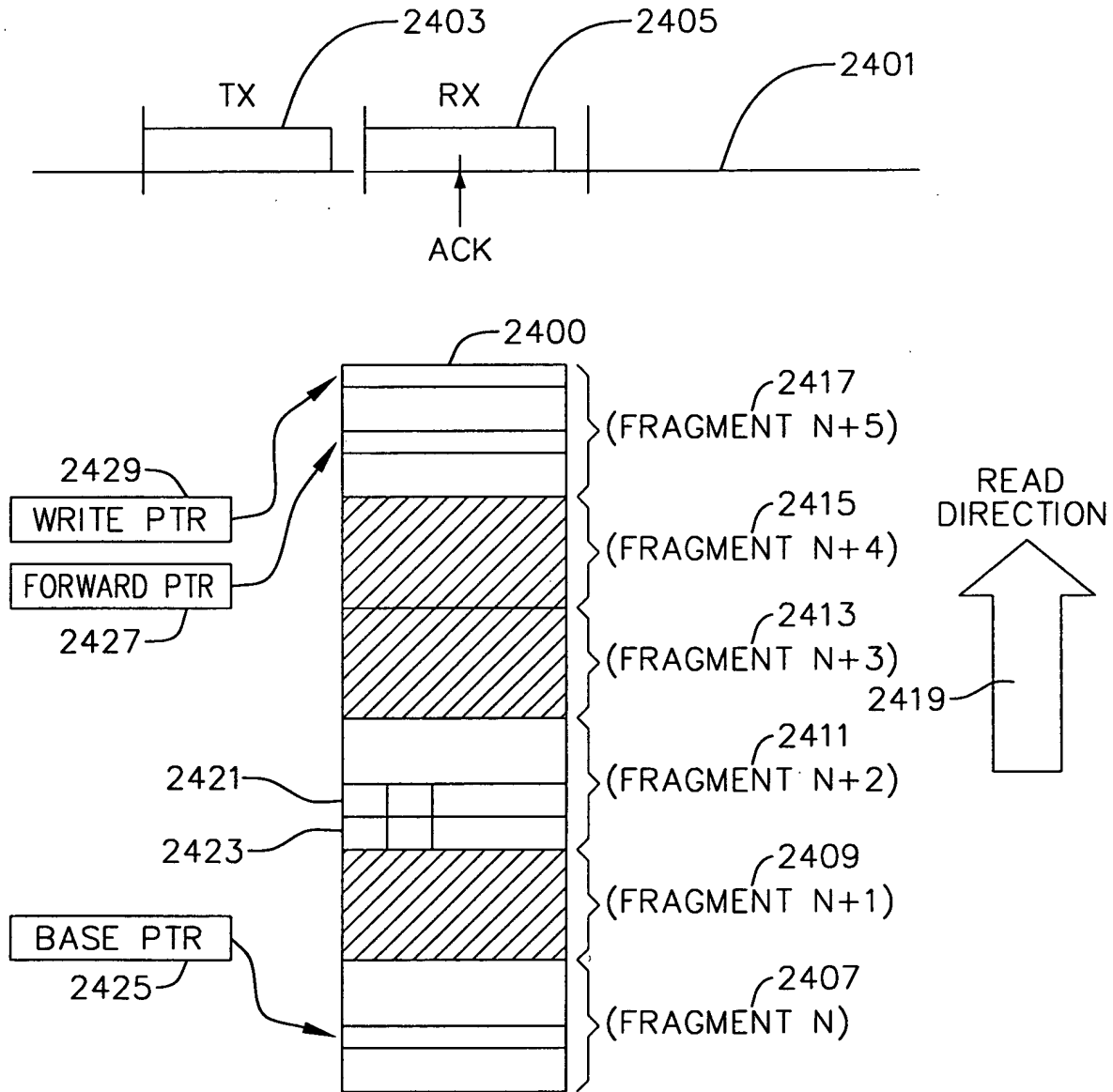


FIG. 24



**FIG. 25**

THE FOLLOWING DIPICTS THE BYTE GAUGE STATE MACHINE IN FLOW  
DIAGRAM FORM. NUMBERS IN PARENTHESES INDICATE THE STATE.

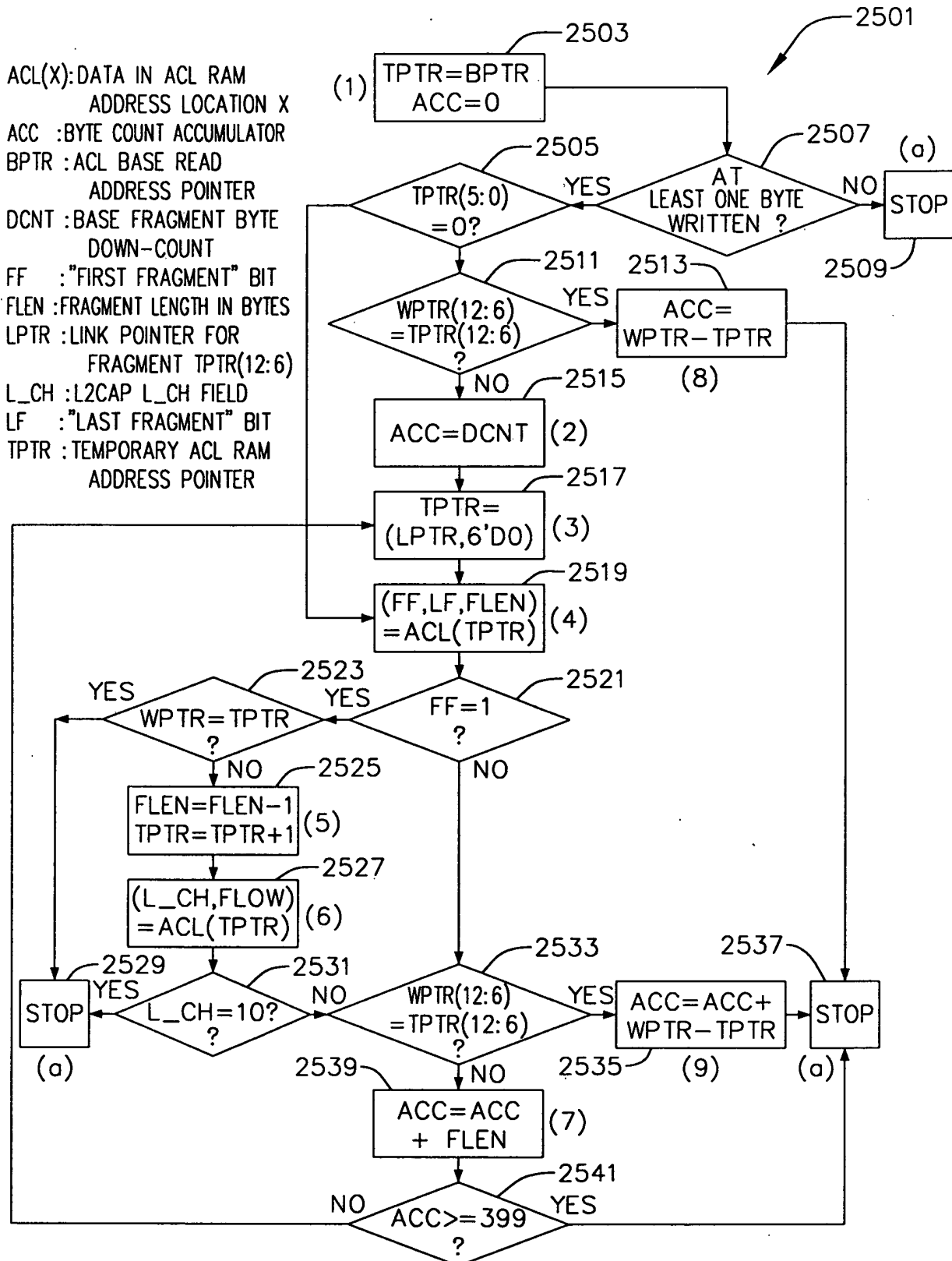


FIG. 26

L2CAP PACKET FLUSH STATE MACHINE

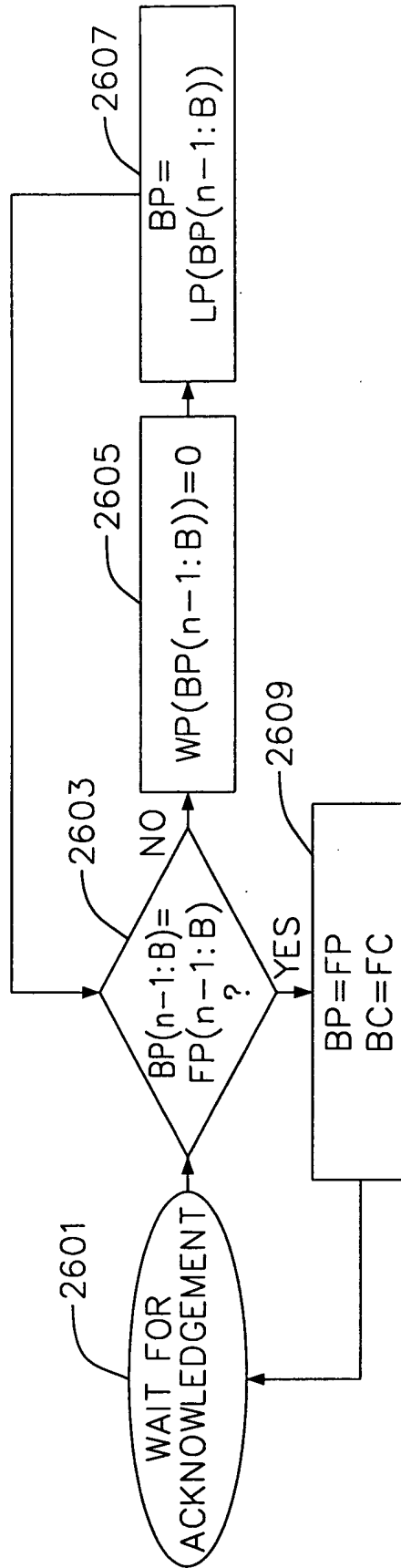




FIG. 27

L2CAP PACKET TRANSMIT STATE MACHINE

KEY:  
BBH1: BASEBAND PACKET HEADER,  
1ST. BYTE  
BBH2: BASEBAND PACKET HEADER,  
2ND. BYTE  
BC : BASE DOWN-COUNT (COUNTS  
DOWN FROM FRAGMENT  
LENGTH)  
BP : BASE POINTER  
FC : FORWARD DOWN-COUNT  
(COUNTS DOWN FROM  
FRAGMENT LENGTH)  
FP : FORWARD POINTER  
LP : LINK POINTER ARRAY  
MEM : L2CAP MEMORY ARRAY  
N : NUMBER OF BITS IN  
ADDRESS TO MEMORY ARRAY  
PB : PAYLOAD BYTE DOWN-COUNT  
(COUNTS DOWN FROM  
PACKET LENGTH)  
PLH1: PAYLOAD HEADER,  
1ST. BYTE  
PLH2: PAYLOAD HEADER,  
2ND. BYTE  
WP : WRITE PROTECTION ARRAY  
--ONE BIT PER FRAGMENT

NOTATION:

-- : DECREMENT BY ONE  
++ : INCREMENT BY ONE  
X(Y) : ELEMENT Y OF ARRAY X  
A(B:C) : BITS B THROUGH C OF  
N BIT BUS A  
(D,E) : CONCATINATION OF D AND E  
6'H : 6-BIT HEX VALUE

